

## Ch 1 terms

*NOTE05 - Terms you need to know from Chapter 1*

*Text reference: **Chapter 1, pages 1-54***

<b>Section 1.1</b>	gates	hardware
program	register	software
machine language	ALU	algorithm
structured computer organization	datapath	microprogram/microcode
translation	assembler	operating system
interpretation	compiler	system call
virtual machine		batch system
		timesharing system
<b>Section 1.2</b>	CPU	multiprogramming
difference engine	accumulator	VLSI
analytical engine	bus	FPGA
von Neumann machine	supercomputer	
<b>Section 1.3</b>	Moore's Law	microcontroller
RFID	virtuous circle	mainframe
<b>Section 1.4</b>	x86	ARM
cache memory	Pentium	AVR
<b>Section 1.5</b>		
kilo, mega, giga, tera, peta	KB, MB, GB, TB	Kbps, Mbps, Gbps, Tbps